



2822

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE

Docket No.: 303.324US2

Serial No.: 09/256,643

Filed: February 23, 1999

Due Date: November 23, 2001

Examiner: Michael Trinh

Group Art Unit: 2822

Commissioner for Patents
Washington, D.C. 20231

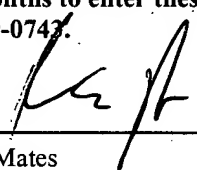
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We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Amendment and Response under 37 CFR 1.111 (7 Pages).
- ☒ Petition for Extension of Time (1 pg.)
- ☒ A check in the amount of \$110.00 to cover the Extension of Time Fee.
- ☒ A Supplemental Information Disclosure Statement (1 pgs.), Form 1449 (1 pgs.), and copies of 2 cited references.
- ☒ A check in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 97(c).

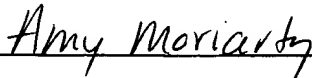
Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
P.O. Box 2938, Minneapolis, MN 55402 (612-373-6900)

By: 
Atty: Robert E. Mates
Reg. No. 35,271

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 21st day of November, 2001.

Name



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